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DISPLAY DEVICE AND METHOD OF
MANUFACTURING THE SAME****Publication Classification**(51) **Int. Cl.**
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MinJu KIM, PAJU-SI (KR)(73) Assignee: **LG DISPLAY CO., LTD.**, Seoul (KR)(21) Appl. No.: **14/504,119**(22) Filed: **Oct. 1, 2014**(30) **Foreign Application Priority Data**

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(57) **ABSTRACT**

Discussed are an organic light emitting diode (OLED) display device and a method of manufacturing the same, which can simplify a manufacturing process and decrease an error. The OLED display device includes a substrate on which an organic light emitting diode (OLED) is formed, a first inorganic thin layer configured to cover the OLED, an organic deposition layer configured to have an organic layer characteristic covering the first inorganic thin layer, and a second inorganic thin layer configured to cover the organic deposition layer.

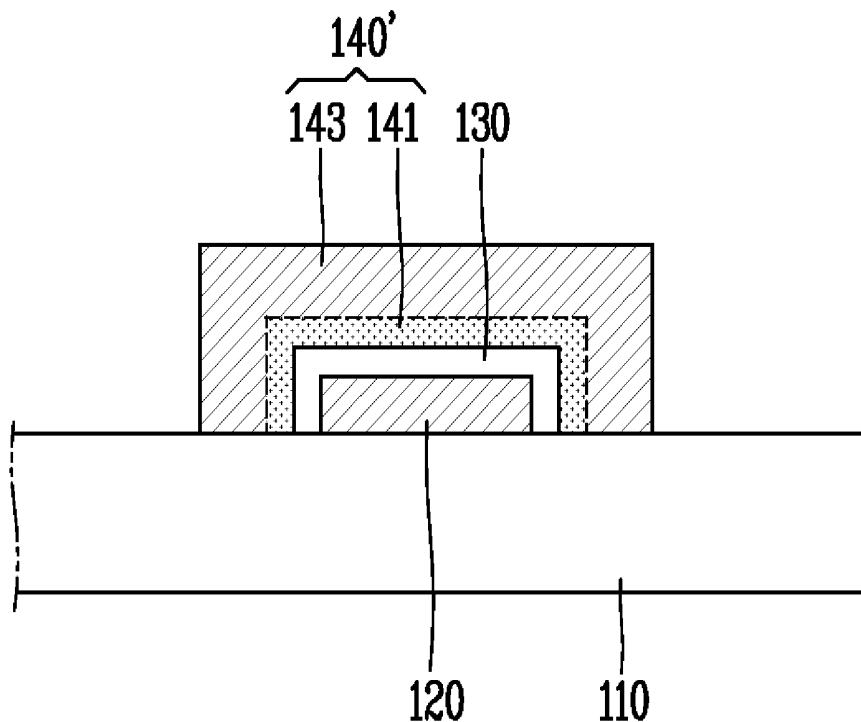


FIG. 1
RELATED ART

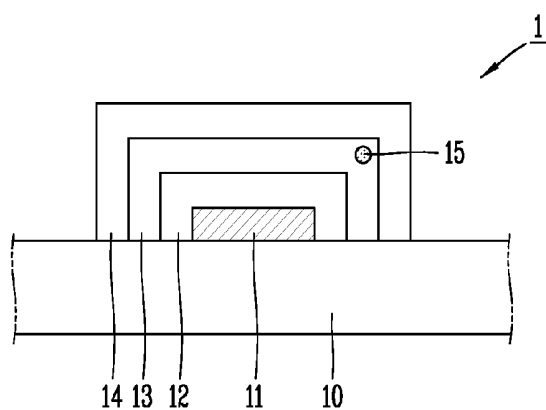


FIG. 2
RELATED ART

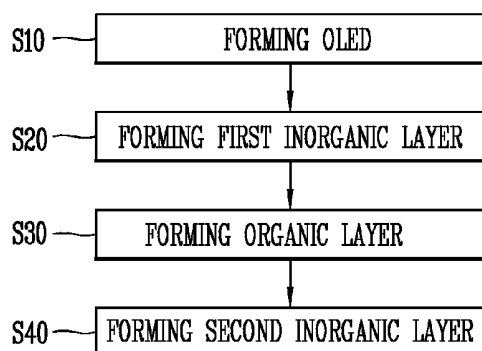


FIG. 3

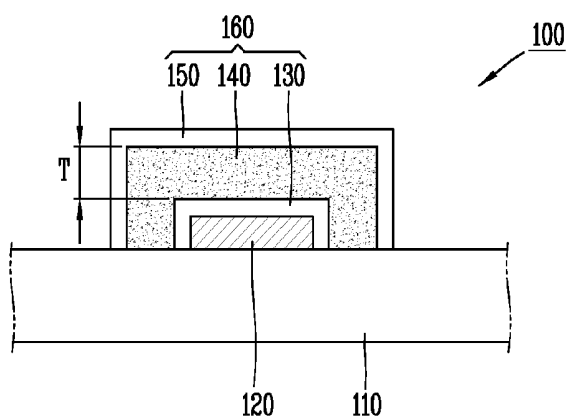


FIG. 4A

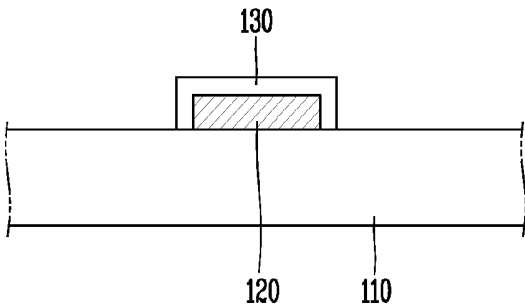


FIG. 4B

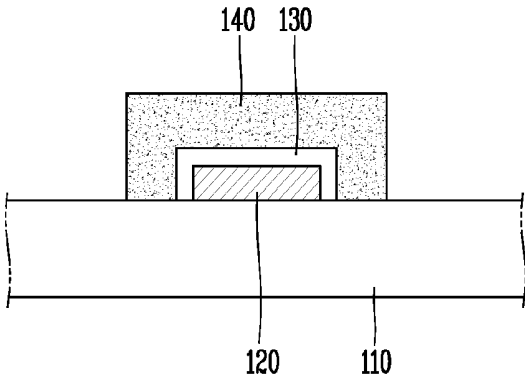


FIG. 4C

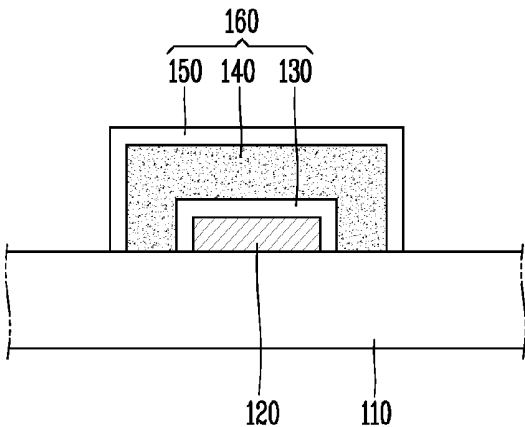


FIG. 5

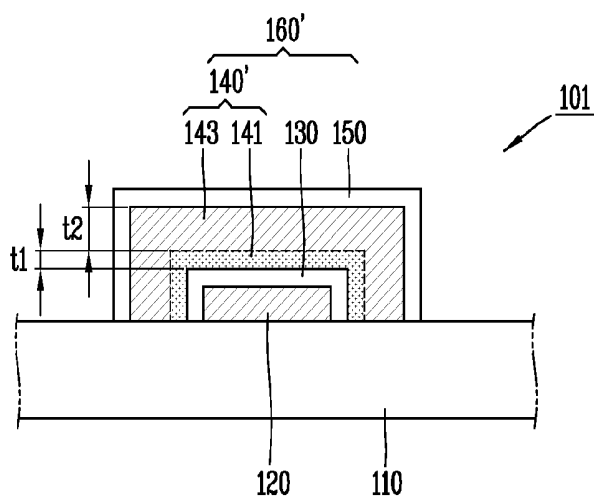


FIG. 6A

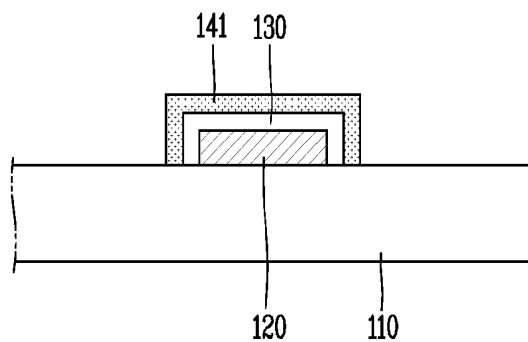
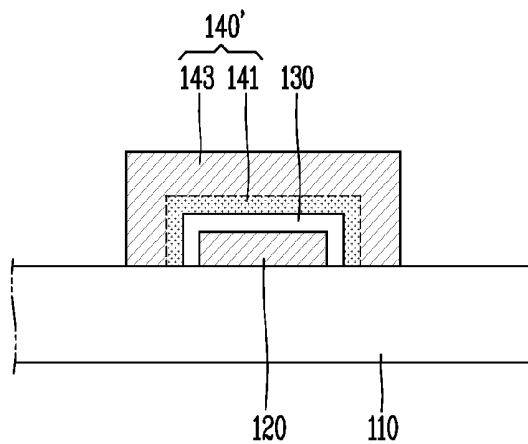


FIG. 6B



ORGANIC LIGHT EMITTING DIODE DISPLAY DEVICE AND METHOD OF MANUFACTURING THE SAME

CROSS-REFERENCE TO RELATED APPLICATION

[0001] Pursuant to 35 U.S.C. §119(a), this application claims the benefit of earlier filing date and right of priority to Korean Application No. 10-2013-0117537, filed on Oct. 1, 2013, the contents of which is incorporated by reference herein in its entirety.

BACKGROUND OF THE DISCLOSURE

[0002] 1. Field of the Disclosure

[0003] The present disclosure relates to an organic light emitting diode (OLED) display device, and particularly, to an OLED display device and a method of manufacturing the same, which can simplify a manufacturing process and decrease an error.

[0004] 2. Background of the Disclosure

[0005] OLED display devices, which control an amount of light emitted from an organic emission layer to display an image, are attracting much attention as flat panel display devices for reducing weight and volume which are drawbacks of cathode ray tubes (CRTs).

[0006] OLED display devices have a structure in which a sub-pixel driver array and an organic emission array are formed on a substrate, and an image is displayed by light emitted from an OLED of the organic emission array. The OLED display devices use a self-emitting element including a thin emission layer formed between electrodes, and thus can be thinned like papers.

[0007] The OLED is deteriorated by an internal cause, and for example, an electrode and an emission layer are deteriorated by oxygen, and deterioration is made by a reaction between the emission layer and an interface. Also, the OLED is easily deteriorated by external causes such as external moisture, oxygen, ultraviolet light, and a manufacturing condition of a device. Therefore, packaging of the OLED is important.

[0008] In the related art, a method of packaging an OLED uses a method which seals a substrate, on which an OLED is formed, with a protective cap. However, since the method should use a separate material such as an adhesive or a moisture absorbent, the material cost increases. Also, since the protective cap is formed, a volume and a thickness of an OLED display device increase. Also, since a material of the protective cap is glass, it is difficult to realize a flexibility of the OLED display device.

[0009] To address such problems, a method which encapsulates an OLED by using a plurality of thin layers is attempted.

[0010] FIG. 1 is a schematic cross-sectional view of a related art OLED display device, and FIG. 2 is a flowchart illustrating a process for the related art OLED display device.

[0011] Referring to FIGS. 1 and 2, a related art OLED display device 1 includes an OLED 11, which is formed on a substrate 10, and an encapsulating layer which is formed by stacking a plurality of thin layers, namely, a first inorganic layer 12, an organic layer 13, and a second inorganic layer, 14 to cover the OLED 11.

[0012] The kind of the substrate 10 is not limited, and for example, the substrate 10 may use a glass substrate, a plastic substrate, or a silicon substrate.

[0013] Moreover, each of the first inorganic layer 12 and the second inorganic layer 14 uses one or a combination of two or more of silicon nitride, silicon oxide, metal, and metal oxide.

[0014] Moreover, the organic layer 13 uses a polymer, which uses acrylate and an imide-based polymer.

[0015] The OLED 11 is formed in a display area of the substrate 10, and although not shown, an organic emission layer (not shown) is formed between a first electrode (not shown) and a second electrode (not shown) in operation S10.

[0016] The substrate 10, on which the OLED 11 is formed, is moved a separate chamber (not shown) for an inorganic layer manufacturing process. In operation S20, the first inorganic layer 12 is formed to cover the OLED 11 by performing processes such as chemical vapor deposition (CVD), plasma enhanced chemical vapor deposition (PECVD), thermal deposition, sputtering deposition, ion beam deposition, electron beam deposition, and atomic layer deposition.

[0017] Subsequently, the substrate 10 on which the first inorganic layer 12 is formed is moved a separate chamber (not shown) for an organic layer manufacturing process. In operation S30, the organic layer 13 is formed to cover the OLED 11 by performing processes such as screen printing, slot printing, and inkjet.

[0018] Subsequently, the substrate 10 on which the inorganic layer 13 is formed is again moved the separate chamber (not shown) for the inorganic layer manufacturing process. In operation S40, the second inorganic layer 14 is formed to cover the organic layer 13 by performing the CVD process or the PECVD process once again, and thus, the OLED 11 is encapsulated.

[0019] However, in the above-described process of manufacturing the related art OLED display device, since a process of forming the inorganic layer differs from a process of forming the organic layer, additional process equipment (for example, printing process equipment) is further needed. For this reason, in the related art OLED display device 1, a manufacturing process is complicated, causing an increase in cost.

[0020] Furthermore, in a printing process of the organic layer 13, a nozzle is blocked, and an undoped case occurs. For this reason, a foreign material 15 occurs in the organic layer 13. Alternatively, due to a stress characteristic difference between different layers (i.e., the organic layer and the inorganic layer), a layer-crumpled phenomenon or a pinhole occurs at a boundary between the different layers.

SUMMARY OF THE DISCLOSURE

[0021] Therefore, an aspect of the detailed description is to provide an OLED display device and a method of manufacturing the same, which can simplify a manufacturing process and decrease an error.

[0022] To achieve these and other advantages and in accordance with the purpose of this specification, as embodied and broadly described herein, an organic light emitting diode (OLED) display device includes: a substrate on which an organic light emitting diode (OLED) is formed; a first inorganic thin layer configured to cover the OLED; an organic deposition layer configured to have an organic layer characteristic covering the first inorganic thin layer; and a second inorganic thin layer configured to cover the organic deposition layer.

[0023] In another aspect of the present invention, a method of manufacturing an organic light emitting diode (OLED) display device includes: forming an organic light emitting diode (OLED) on a substrate; forming a first inorganic thin layer to cover the OLED; forming an organic deposition layer having an organic layer characteristic to cover the first inorganic thin layer; and forming a second inorganic thin layer to cover the organic deposition layer, wherein the first inorganic thin layer, the organic deposition layer, and the second inorganic thin layer are formed by the same process.

[0024] Further scope of applicability of the present application will become more apparent from the detailed description given hereinafter. However, it should be understood that the detailed description and specific examples, while indicating preferred embodiments of the disclosure, are given by way of illustration only, since various changes and modifications within the spirit and scope of the disclosure will become apparent to those skilled in the art from the detailed description.

BRIEF DESCRIPTION OF THE DRAWINGS

[0025] The accompanying drawings, which are included to provide a further understanding of the disclosure and are incorporated in and constitute a part of this specification, illustrate exemplary embodiments and together with the description serve to explain the principles of the disclosure.

[0026] In the drawings:

[0027] FIG. 1 is a schematic cross-sectional view of a related art OLED display device;

[0028] FIG. 2 is a flowchart illustrating a process for the related art OLED display device;

[0029] FIG. 3 is a schematic cross-sectional view of an OLED display device according to an embodiment of the present invention;

[0030] FIGS. 4A to 4C are process views of the OLED display device of FIG. 3;

[0031] FIG. 5 is a schematic cross-sectional view of an OLED display device according to another embodiment of the present invention; and

[0032] FIGS. 6A and 6B are process views of the OLED display device of FIG. 5.

DETAILED DESCRIPTION OF THE DISCLOSURE

[0033] Description will now be given in detail of the exemplary embodiments, with reference to the accompanying drawings. For the sake of brief description with reference to the drawings, the same or equivalent components will be provided with the same reference numbers, and description thereof will not be repeated.

[0034] Hereinafter, an OLED display device and a method of manufacturing the same according to embodiments of the present invention will be described in detail with reference to the accompanying drawings.

[0035] FIG. 3 is a schematic cross-sectional view of an OLED display device according to an embodiment of the present invention.

[0036] Referring to FIG. 3, an OLED display device 100 according to an embodiment of the present invention may include an OLED 120, which is formed on a substrate 110, and an encapsulating layer 160 which is formed of a plurality

of thin layers 130, 140 and 150 and encapsulates the OLED 120. All the components of the device 100 are operatively coupled and configured.

[0037] The substrate 110 may use a transparent substrate which is formed of glass, plastic, or a conductive material.

[0038] A buffer layer (not shown) for protecting the OLED 120 may be further formed on the substrate 110. The buffer layer may be formed of SiO₂ or SiN_x.

[0039] The OLED 120 is driven by a cell driving array (not shown) which is formed on the substrate 110, and the cell driving array may be configured with a plurality of sub-pixel drivers.

[0040] Each of the plurality of sub-pixel drivers may include a plurality of signal lines which include a gate line and a data line, a transistor, a capacitor, and a plurality of insulating layers. The transistor may include a switching transistor and a driving transistor.

[0041] Here, the switching transistor may transfer a data signal, which is applied from the data line, to the driving transistor in response to a gate signal applied from the gate line. Also, the driving transistor may control an amount of current flowing in the OLED 120, in response to the data signal supplied from the switching transistor. Also, even when the switching transistor is turned off, the capacitor may allow a constant current to flow in the OLED 120 through the driving transistor.

[0042] The OLED 120 may emit red light, green light, and blue light according to a flow of a current supplied from the sub-pixel driver, thereby displaying image information. The OLED 120 may be driven in an active matrix type or a passive matrix type.

[0043] The OLED 120 may include a first electrode connected to the driving transistor, a second electrode which is an electrode opposite to the first electrode, and an organic emission layer which is disposed therebetween and emits light.

[0044] Here, the first electrode may be an anode electrode, and the second electrode may be a cathode electrode. The first electrode may be insulated from the second electrode. The first electrode and the second electrode may apply voltages having different polarities to the organic emission layer, and thus allow light to be emitted from the organic emission layer.

[0045] When the OLED display device 100 according to an embodiment of the present invention is manufactured in a bottom emission type, the first electrode may be formed of a transparent conductive layer. The transparent conductive layer may be formed of indium tin oxide (ITO), tin oxide (TO), indium zinc oxide (IZO), indium tin zinc oxide (ITZO), or a combination thereof. The second electrode may be formed all over the substrate 110, and may be formed of metal materials such as chromium (Cr), aluminum (Al), and molybdenum (Mo), an alloy thereof, or oxide. The second electrode may be formed of multi layers including two or more layers.

[0046] Moreover, when the OLED display device 100 is manufactured in a top emission type, the first electrode may be formed of metal materials such as Cr, Al, and Mo, an alloy thereof, or oxide, and may be formed of multi layers including two or more layers. The second electrode may be formed of a transparent conductive layer such as ITO or IZO.

[0047] The organic emission layer is a layer in which a positive hole and an electron respectively injected from the first electrode and the second electrode are combined to emit light. The organic emission layer may include a hole injection

layer (HIL), a hole transporting layer (HTL), an emission layer (EML), an electron transporting layer (ETL), and an electron injection layer (EIL).

[0048] The OLED 120 may be divided in units of a pixel unit by an insulating layer such as a bank (not shown), and may display an image by using the principle that light emitted from the organic emission layer is output to the outside through a transparent substrate or a transparent electrode.

[0049] The OLED 120 is vulnerable to external moisture or oxygen, and thus, an encapsulating layer 160 for protecting the OLED 120 may be formed on the OLED 120.

[0050] The encapsulating layer 160 may be configured with one or more thin layers which are stacked on the OLED 120. In the present embodiment, a case in which a first thin layer 30, a second thin layer 140, and a third thin layer 150 are stacked to form the encapsulating layer 160 will be described as an example. The encapsulating layer 160 may be formed on the OLED 120 to have a thickness of about 1 μm to about 10 μm .

[0051] The first thin layer 130 of the encapsulating layer 160 is an inorganic layer, and may be formed just on the OLED 120 to cover the OLED 120. The first thin layer 130 may be formed of multi layers formed of one or a combination of two or more of silicon nitride, silicon oxide, metal, and metal oxide such as Al_2O_3 , AlON, MgO, ZnO, HfO_2 , ZrO_2 . The first thin layer 130 may be formed on the OLED 120 to a thickness of about 0.1 μm to about 1 μm by a CVD process or a PECVD process.

[0052] The second thin layer 140 may be formed on the first thin layer 130 to cover the first thin layer 130. The second thin layer 140 may be an organic deposition layer such as a SiOC layer including carbon or a SiOCH layer including carbon or oxygen. The second thin layer 140 has a characteristic of an organic layer, and thus has a good covering characteristic to the first thin layer 130 or the OLED 120.

[0053] The second thin layer 140 may be formed by the same process as the above-described process of the first thin layer 130, namely, the CVD process or the PECVD process. The second thin layer 140 may be formed on the first thin layer 130 to have a thickness T of 1 μm to 10 μm .

[0054] The third thin layer 150 is an inorganic thin layer which is the same as the first thin layer 130. The third thin layer 150 may be formed by the same process as the above-described process of the first thin layer 130, namely, the CVD process or the PECVD process, to cover the second thin layer 140. The third thin layer 150 may be formed on the second thin layer 140 to have a thickness T of 0.1 μm to 1 μm .

[0055] As described above, the encapsulating layer 160 is formed by the plurality of thin layers (i.e., the first thin layer 130, the second thin layer 140, and the third layer 150) to cover the OLED 120 which is formed on the substrate 110, thereby preventing moisture or air from penetrating into the OLED 120 from the outside.

[0056] Hereinafter, a method of manufacturing an OLED display device according to an embodiment of the present invention will be described in detail.

[0057] FIGS. 4A to 4C are process views of the OLED display device of FIG. 3.

[0058] Referring to FIG. 4A, the substrate 110 on which the OLED 120 is formed may be prepared, and then, the first thin layer 130 may be formed to cover the OLED 120.

[0059] The first thin layer 130 may be formed on the OLED 120 by a deposition process such as the CVD process or the PECVD process. The first thin layer 130 may be formed of

multi layers formed of one or a combination of two or more of silicon nitride, silicon oxide, metal, and metal oxide such as Al_2O_3 , AlON, MgO, ZnO, HfO_2 , ZrO_2 . The first thin layer 130 may be deposited and formed on the OLED 120 to a thickness of about 1 μm .

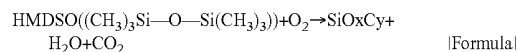
[0060] Moreover, although not shown, the buffer layer may be further formed between the substrate 110 and the OLED 120. Also, a passivation layer (not shown) may be further formed on the second electrode of the OLED 120 to sufficiently cover the second electrode.

[0061] Referring to FIG. 4B, the second thin layer 140 may be formed to cover the first thin layer 130.

[0062] The second thin layer 140 may be formed by the same process as the process of the first thin layer 130, namely, the CVD process or the PECVD process. The second thin layer 140 may be formed by causing a chemical reaction of a polymer monomer through the CVD process or the PECVD process.

[0063] Here, the polymer monomer may use Hexamethyldisiloxane (HMDSO), Tetramethyldisiloxane (TMDSO), TMMOS(CH_3)₃SiOCH₃, Bistrimethylsilylmethane (BT-MSM), Tetraethoxysilane (TEOS), DVTMDSO[(CH_3)₂ViSi-0-SiVi(CH_3)₂], or Octamethylcyclotetrasiloxan (OM-CATS). Also, oxygen (O_2) or hydrogen (H_2) may be used as a medium for the chemical reaction of the polymer monomer.

[0064] For example, when HMDSO is used as the polymer monomer, a SiOC layer is formed by reacting HMDSO as expressed in the following chemical reaction formula, and the second thin layer 140 may be formed by depositing the SiOC layer on the first thin layer 130.



[0065] Here, in a process condition for forming the second thin layer 140, a temperature of a chamber (or a stage) may be a room temperature to 100 degrees C., a pressure of the chamber may be 0.4 Torr to 1.6 Torr, and a flow rate of O_2 may be 100 sccm to 1,000 sccm.

[0066] Moreover, a deposition rate (D/R) of the second thin layer 140 may be 20,000 $\text{\AA}/\text{min}$ or more. About 0.5 minutes to 2.5 minutes may be expended when the second thin layer 140 is formed on the first thin layer 130 to have about 1 μm to about 10 μm .

[0067] Subsequently, the deposited second thin layer 140 may be cured. This is for preventing an error, such as a layer being crumpled, from occurring at a boundary between two layers due to a hardness difference between the second thin layer 140 and the below-described third thin layer 150. The second thin layer 140 may be cured for about 10 minutes to about 200 minutes under a temperature of about 30 degrees C. to about 100 degrees C.

[0068] Referring to FIG. 4C, the third thin layer 150 may be formed to cover the second thin layer 140.

[0069] Similarly to the above-described first thin layer 130, the third thin layer 150 may be formed by a deposition process such as the CVD process or the PECVD process. The third thin layer 150 may be formed of multi layers formed of one or a combination of two or more of silicon nitride, silicon oxide, metal, and metal oxide such as Al_2O_3 , AlON, MgO, ZnO, HfO_2 , ZrO_2 .

[0070] As described above, in the OLED display device 100 according to an embodiment of the present invention, the plurality of thin layers of the encapsulating layer 160 covering the OLED 120 are all formed by a single process, namely, the CVD process or the PECVD process, and thus, a manu-

[0072] Except that a second thin layer **140'** of an encapsulating layer **160'** is formed of a double layer, an OLED display device **101** illustrated in FIG. **5** has the same configuration as that of the OLED display device **100** of FIG. **3**. Therefore, like elements are referred to by like reference numbers refer to like elements, and thus, their detailed descriptions are not repeated.

[0074] The OLED 120 may include a first electrode, a second electrode which is an electrode opposite to the first electrode, and an organic emission layer which is disposed therebetween and emits light.

[0076] The encapsulating layer 160' may include a first thin layer 130, a second thin layer 140', a third thin layer 150 which are stacked on the OLED 120. Here, each the first thin layer 130 and the third thin layer 150 is an inorganic thin layer, and the second thin layer 140' therebetween may be an organic deposition layer having a characteristic of an organic layer.

[0078] Here, the first layer **141** of the second thin layer **140'** may be a flexible organic layer which has a good covering characteristic to the first thin layer **130** or the OLED **120**, and the second layer **143** may be a non-flexible organic layer which has a high hardness characteristic for decreasing a hardness difference with the third thin layer **150**. The first layer **141** and second layer **143** of the second thin layer **140'** may be successively formed in a process of forming the second thin layer **140'**.

[0080] Here, the first layer 141 of the second thin layer 140' may be formed to have a higher content of carbon than that of the second layer 143. A content of carbon of the first layer 141 is about 30% to about 50%, and a content of carbon of the second layer 143 is about 0% to about 30%.

layer **140'** may be formed to a thickness **t1** which is equal to or less than 70% of a total thickness of the second thin layer **140'**, and the second layer **143** may be formed to a thickness **t2** which is equal to or greater than 30% of the total thickness of the second thin layer **140'**. For example, when the total thickness of the second thin layer **140'** is 5 μm , the thickness **t1** of the first layer **141** may be about 1 μm to about 3.5 μm , and the thickness **t2** of the second layer **143** may be about 1.5 μm to about 4 μm .

[0083] In the present embodiment, a case in which the second thin layer 140' is formed by stacking the first layer 141 (which is the flexible organic layer) and the second layer 143 (which is the non-flexible organic layer) has been described above as an example, and the present embodiment is not limited thereto. For example, the second thin layer 140' may have a triple-layer structure in which two non-flexible organic layers and a flexible organic layer therebetween are stacked, and may have a structure which is stacked in the order of a non-flexible organic layer and a flexible organic layer.

[0086] FIGS. 6A and 6B are process views of the OLED display device of FIG. 5.

[0087] First, as described above with reference to FIG. 4A, the substrate 110 on which the OLED 120 is formed may be prepared, and then, the first thin layer 130 may be formed to cover the OLED 120.

[0088] The first thin layer **130** may be formed on the OLED **120** by a deposition process such as the CVD process or the PECVD process. The first thin layer **130** may be formed of multi layers formed of one or a combination of two or more of silicon nitride, silicon oxide, metal, and metal oxide such as Al_2O_3 , AlON, MgO , ZnO , HfO_2 , ZrO_2 . The first thin layer **130** may be deposited and formed on the OLED **120** to a thickness of about $1\ \mu\text{m}$.

[0089] Referring to FIGS. 6A and 6B, the second thin layer 140' is formed to cover the first thin layer 130, and the first layer 141 and second layer 143 of the second thin layer 140' may be formed by a successive process.

[0090] The second thin layer **140'** may be formed by causing a chemical reaction of a polymer monomer through the CVD process or the PECVD process. The polymer monomer may use HMDSO, TMDSO, TMMOS(CH₃)₃SiOCH₃, BTMSM, TEOS, DVTMDSO[(CH₃)₂ViSi-O-SiVi(CH₃)₂], or OMCATS. Also, in a process condition for forming the second thin layer **140'**, a temperature may be a room temperature to 100 degrees C., a pressure may be 0.4 Torr to 1.6 Torr, and a flow rate of O₂ may be 100 sccm to 1,000 sccm.

[0091] The first layer **141** and second layer **143** of the second thin layer **140'** may be successively formed by differ-

ently setting a process condition in the above-described chemical reaction of the polymer monomer.

[0092] In other words, in the first layer **141** of the second thin layer **140'**, a content of carbon may be set to 30% to 50% by adjusting at least one of a temperature, a pressure, and a flow rate of O₂ in the above-described chemical reaction of the polymer monomer. Here, the first layer **141** of the second thin layer **140'** may be deposited on the first thin layer **130** to about 1 μm to about 3.5 μm.

[0093] When deposition of the first layer **141** is completed, the second layer **143** in which a content of carbon is 0% to 30% may be formed by adjusting at least one of a temperature, a pressure, and a flow rate of O₂ once again. Here, the second layer **143** of the second thin layer **140'** may be deposited on the first layer **141** to about 1.5 μm to about 4 μm.

[0094] That is, in the present embodiment, the second thin layer **140'** is successively formed in a double-layer structure, and thus, a process of curing the second thin layer **140** described above with reference to FIG. 4B may be omitted. Accordingly, a manufacturing process can be further simplified.

[0095] Moreover, a hardness of the second layer **143** of the second thin layer **140'** increases due to a content of carbon, and thus, a layer-crumpled phenomenon can be prevented from occurring at a boundary between the second layer **143** and the third thin layer **150** contacting the second layer **143**.

[0096] Here, the first layer **141** of the second thin layer **140'** may be a flexible organic layer, and the second layer **143** may be a non-flexible organic layer. Also, the second thin layer **140'** may have a triple-layer structure in which two non-flexible organic layers and a flexible organic layer therebetween are stacked, and may have a structure which is stacked in the order of a non-flexible organic layer and a flexible organic layer.

[0097] As described above, when the second thin layer **140'** having a double-layer structure of the first layer **141** and the second layer **143** is formed, as described above with reference to FIG. 4C, the third thin layer **150** may be formed to cover the second thin layer **140'**.

[0098] Similarly to the above-described first thin layer **130**, the third thin layer **150** may be formed by the deposition process such as the CVD process or the PECVD process. The third thin layer **150** may be formed of multi layers formed of one or a combination of two or more of silicon nitride, silicon oxide, metal, and metal oxide such as Al₂O₃, AlON, MgO, ZnO, HfO₂, ZrO₂.

[0099] In the OLED display device and the method of manufacturing the same according to the embodiments of the present invention, the plurality of thin layers included in the encapsulating layer which protects the OLED are simultaneously formed by the same process, and thus, a process of manufacturing the OLED display device can be simplified, thereby reducing the manufacturing cost.

[0100] Moreover, an image quality and an appearance defect of the OLED display device can be reduced by removing a pinhole or a layer-crumpled phenomenon which occurs at a boundary between different layers of the encapsulating layer.

[0101] The foregoing embodiments and advantages are merely exemplary and are not to be considered as limiting the present disclosure. The present teachings can be readily applied to other types of apparatuses. This description is intended to be illustrative, and not to limit the scope of the claims. Many alternatives, modifications, and variations will

be apparent to those skilled in the art. The features, structures, methods, and other characteristics of the exemplary embodiments described herein may be combined in various ways to obtain additional and/or alternative exemplary embodiments.

[0102] As the present features may be embodied in several forms without departing from the characteristics thereof, it should also be understood that the above-described embodiments are not limited by any of the details of the foregoing description, unless otherwise specified, but rather should be considered broadly within its scope as defined in the appended claims, and therefore all changes and modifications that fall within the metes and bounds of the claims, or equivalents of such metes and bounds are therefore intended to be embraced by the appended claims.

What is claimed is:

1. An organic light emitting diode (OLED) display device comprising:

an organic light emitting diode (OLED) formed on a substrate;

a first inorganic thin layer configured to cover the OLED;

an organic deposition layer configured to have an organic layer characteristic covering the first inorganic thin layer; and

a second inorganic thin layer configured to cover the organic deposition layer,

wherein the first inorganic thin layer, the organic deposition layer, and the second inorganic thin layer are formed by the same process.

2. The OLED display device of claim 1, wherein a thickness of the organic deposition layer is 1 μm to 10 μm.

3. The OLED display device of claim 1, wherein the organic deposition layer has a double-layer structure, and

wherein a first layer of the organic deposition layer is formed to 70% or less of a total thickness of the organic deposition layer, and a second layer of the organic deposition layer is formed to 30% or more of a total thickness of the organic deposition layer.

4. A method of manufacturing an organic light emitting diode (OLED) display device, the method comprising:

forming an organic light emitting diode (OLED) on a substrate;

forming a first inorganic thin layer to cover the OLED;

forming an organic deposition layer having an organic layer characteristic to cover the first inorganic thin layer; and

forming a second inorganic thin layer to cover the organic deposition layer,

wherein the first inorganic thin layer, the organic deposition layer, and the second inorganic thin layer are formed by the same process.

5. The method of claim 4, wherein the forming of the organic deposition layer comprises depositing a layer, which is formed by causing a chemical reaction of a polymer monomer, on the first inorganic thin layer.

6. The method of claim 5, wherein,

the polymer monomer comprises one of HMDSO, TMDSO, TMMOS(CH₃)₃SiOCH₃, BTMSM, TEOS, DVTMDSO[(CH₃)₂ViSi-0-SiVi(CH₃)₂], and OMCATS, and

the polymer monomer is chemically reacted with one of O₂ and H₂.

7. The method of claim 4, wherein the forming of the organic deposition layer comprises forming the organic deposition layer on the first inorganic thin layer to have a thickness of 1 μm to 10 μm .

8. The method of claim 4, further comprising, after the organic deposition layer is formed, curing the organic deposition layer.

9. The method of claim 4, wherein,

the organic deposition layer has a double-layer structure, and

the forming of the organic deposition layer comprises:

forming a first layer of the organic deposition layer on the first inorganic thin layer; and

forming a second layer of the organic deposition layer on the first layer.

10. The method of claim 9, wherein the forming of the organic deposition layer comprises forming the first and second layers of the organic deposition layer to have different contents of carbon.

11. The method of claim 10, wherein,

the first layer is formed to have a carbon content of 30% to 50%, and

the second layer is formed to have a carbon content of 0% to 30%.

12. The method of claim 9, wherein the first layer is formed to 70% or less of a total thickness of the organic deposition layer.

13. The method of claim 9, wherein the second layer is formed to 30% or more of a total thickness of the organic deposition layer.

14. The method of claim 4, wherein the first inorganic thin layer, the organic deposition layer, and the second inorganic thin layer are formed by a CVD process or a PECVD process.

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专利名称(译)	有机发光二极管显示装置及其制造方法		
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[标]申请(专利权)人(译)	乐金显示有限公司		
申请(专利权)人(译)	LG DISPLAY CO. , LTD.		
当前申请(专利权)人(译)	LG DISPLAY CO. , LTD.		
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摘要(译)

讨论了有机发光二极管 (OLED) 显示装置及其制造方法，其可以简化制造工艺并减少误差。 OLED显示装置包括：基板，其上形成有机发光二极管 (OLED) ;第一无机薄层，被配置为覆盖OLED;有机沉积层，被配置为具有覆盖第一无机薄层的有机层特性，以及第二无机薄层，被配置为覆盖有机沉积层。

Discussed are an organic light emitting diode (OLED) display device and a method of manufacturing the same, which can simplify a manufacturing process and decrease an error. The OLED display device includes a substrate on which an organic light emitting diode (OLED) is formed, a first inorganic thin layer configured to cover the OLED, an organic deposition layer configured to have an organic layer characteristic covering the first inorganic thin layer, and a second inorganic thin layer configured to cover the organic deposition layer.